

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: Jeffrey Jay Scheel

Serial No.: 09/672,043

Filed: September 29, 2000

For: Technique for Configuring Processors in System with Logical Partitions

Group Art Unit: 2123

Confirmation No.: 1185

**NOTICE OF APPEAL**

FILED VIA FAX  
Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicant hereby appeals to the Board of Appeals from the decision dated June 15, 2004, of the Primary Examiner finally rejecting claims 1-14.

The item(s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection was granted on \_\_\_\_\_ for \_\_\_\_\_ month(s).
2. ☐ A timely response to the final rejection has been filed.
3. ☒ This Notice of Appeal constitutes a timely response to the final rejection.

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4. ☒ The fee of \$330.00 is:
- ☐ Enclosed.
- ☐ Not required. (Fee paid in prior appeal.)
- ☒ Charged to Deposit Account No. 09-0465. A duplicate copy of this sheet is enclosed.

Date: September 14, 2004

<p><b>CERTIFICATE UNDER 37 CFR 1.9(a)</b></p> <p>I hereby certify that the enclosed or attached correspondence is being transmitted via facsimile to the Commissioner for Patents, 703-672-9306, on</p> <p><u>September 15, 2004</u></p> <p>Date of Deposit</p> <p><u>Owen J. Gamon</u></p> <p>Owen J. Gamon</p>
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Respectfully submitted,

By Owen J. Gamon  
 Owen J. Gamon  
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From:

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